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prepared. This is easily formalised using the **partial trace**. Consider a Hilbert space H decomposing

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Instruction Duration Estimation by **Partial Trace** Evaluation Matteo Corti ETH Zurich
the WCET of the program's methods using **partial trace** evaluation (see Section 3)3. **Partial trace**
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executable takes 133.4Kb memory. Here is a **partial trace** of an execution of the coordiante frame
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is a prefix of some trace t T is called a **partial trace** (for T)By T we denote the set of partial
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system, and the field, respectively. The **partial trace** tr f is defined by the requirement tr(Atr f
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be chained can be expressed easily in terms of **partial traces**. It turns out that the proof of the quantum
its range projector and tr [k,l] A) is the **partial trace** of A over the local algebra A k,l] A
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1 An automatic trace analysis tool generator for Estelle specifications

S. Alan Ezust, Gregor v. Bochmann
October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '95**, Volume 25 Issue 4
Publisher: ACM Press

Full text available: pdf(1.15 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the development of Tango, an automatic generator of backtracking trace analysis tools for single-process specifications written in the formal description language, Estelle. A tool generated by Tango automatically checks the validity of any execution trace against the given specification, and supports a number of checking options. The approach taken was to modify an Estelle-to-C++ compiler. Discussion about nondeterministic specifications, multiple observation points, and on- ...

2 Enforcing trace properties by program transformation

Thomas Colcombet, Pascal Fradet
January 2000 **Proceedings of the 27th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**
Publisher: ACM Press

Full text available: pdf(1.51 MB) Additional Information: full citation, abstract, references, citings, index terms

We propose an automatic method to enforce trace properties on programs. The programmer specifies the property separately from the program; a program transformer takes the program and the property and automatically produces another "equivalent" pogram satisfying the property. This separation of concerns makes the program easier to develop and maintain. Our approach is both static and dynamic. It integrates static analyses in order to avoid useless transformations. On the other ha ...

3 Reliable communication over unreliable channels

Yehuda Afek, Hagit Attiya, Alan Fekete, Michael Fischer, Nancy Lynch, Yishay Mansour, Dai-Wei Wang, Lenore Zuck
November 1994 **Journal of the ACM (JACM)**, Volume 41 Issue 6
Publisher: ACM Press

Full text available: pdf(2.30 MB) Additional Information: full citation, references, citings, index terms, review

Keywords: FIFO layer, bounded packet header, datalink layer, fault recovery, layer implementation, layered communication protocol, message reordering, packet-switching network, sequence transmission problem, transport protocol

4 Tracing piece by piece: affordable debugging for lazy functional languages

Henrik Nilsson

September 1999 **ACM SIGPLAN Notices , Proceedings of the fourth ACM SIGPLAN international conference on Functional programming ICFP '99**, Volume 34 Issue 9

Publisher: ACM Press

Full text available: pdf(1.48 MB) Additional Information: full citation, abstract, references, citings, index terms

The advantage of lazy functional languages is that programs may be written declaratively without specifying the exact evaluation order. The ensuing order of evaluation can however be quite involved which makes it difficult to debug such programs using traditional, operational techniques. A solution is to trace the computation in a way which focuses on the declarative aspects and hides irrelevant operational details. The main problem with this approach is the immense cost in time and space of tra ...

5 The use of examples in program construction and debugging

Alan W. Biermann

January 1975 **Proceedings of the 1975 annual conference**

Publisher: ACM Press

Full text available: pdf(445.33 KB) Additional Information: full citation, abstract, references, citings, index terms

Techniques are described for automatically creating a computer program from example calculations which are done in scratch pad fashion at a computer display. The correct program is synthesized even though certain indexing instructions are omitted by the user as he executes the sample calculation. The created program can be tested, debugged, and modified by running examples at the display terminal, observing the program behavior, and forcing by hand a change in behavior if errors are observe ...

6 Process semantics: universal axioms compositional rules, and applications

van Vicious Nguyen, Rob Strom

January 1988 **Proceedings of the seventh annual ACM Symposium on Principles of distributed computing PODC '88**

Publisher: ACM Press

Full text available: pdf(1.59 MB) Additional Information: full citation, references, citings, index terms

7 The semantic foundations of concurrent constraint programming

Vijay A. Saraswat, Martin Rinard, Prakash Panangaden

January 1991 **Proceedings of the 18th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**

Publisher: ACM Press

Full text available: pdf(2.33 MB) Additional Information: full citation, references, citings, index terms

8 The role of trace abstractions in program specialization algorithms

J. P. Gallagher, L. Lafave

September 1998 **ACM Computing Surveys (CSUR)**

Publisher: ACM Press

Full text available: pdf(141.46 KB) Additional Information: full citation, references, index terms

9 The dual DFA learning problem (extended abstract): hardness results for programming by demonstration and learning first-order representations

William W. Cohen

January 1996 **Proceedings of the ninth annual conference on Computational learning theory**

Publisher: ACM Press
Full text available: pdf(1.25 MB) Additional Information: full citation, references, citings, index terms

10 Generation and analysis of very long address traces

Anita Borg, R. E. Kessler, David W. Wall
May 1990 **ACM SIGARCH Computer Architecture News , Proceedings of the 17th annual international symposium on Computer Architecture ISCA '90**, Volume 18 Issue 3a
Publisher: ACM Press
Full text available: pdf(1.08 MB) Additional Information: full citation, abstract, references, citings, index terms

Existing methods of generating and analyzing traces suffer from a variety of limitations including complexity, inaccuracy, short length, inflexibility, or applicability only to CISC machines. We use a trace generation mechanism based on link-time code modification which is simple to use, generates accurate long traces of multi-user programs, runs on a RISC machine, and can be flexibly controlled. On-the-fly analysis of the traces allows us to get accurate performance data for large second-l ...

11 Garbage collecting the world: one car at a time

Richard L. Hudson, Ron Morrison, J. Eliot B. Moss, David S. Munro
October 1997 **ACM SIGPLAN Notices , Proceedings of the 12th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications OOPSLA '97**, Volume 32 Issue 10
Publisher: ACM Press
Full text available: pdf(1.94 MB) Additional Information: full citation, abstract, references, citings, index terms

A new garbage collection algorithm for distributed object systems, called DMOS (Distributed. Mature Object Space), is presented. It is derived from two previous algorithms, MOS (Mature Object Space), sometimes called the train algorithm, and PMOS (Persistent Mature Object Space). The contribution of DMOS is that it provides the following unique combination of properties for a distributed collector: safety, completeness, non-disruptiveness, incrementality, and scalability. Furthermore, the DMOS c ...

12 A new framework for exhaustive and incremental data flow analysis using DJ graphs

Vugranam C. Sreedhar, Guang R. Gao, Yong-Fong Lee
May 1996 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1996 conference on Programming language design and implementation PLDI '96**, Volume 31 Issue 5
Publisher: ACM Press
Full text available: pdf(1.41 MB) Additional Information: full citation, abstract, references, citings, index terms

We present a new elimination-based framework for exhaustive and incremental data flow analysis using the DJ graph representation of a program. Unlike the previous approaches to elimination-based incremental data flow analysis, our approach can handle arbitrary non-structural and structural changes to program flowgraphs, including those causing irreducibility. We show how our approach is related to (iterated) dominance frontiers, and exploit this relationship to establish the complexity of our ex ...

13 Cache Memories

Alan Jay Smith
September 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 3
Publisher: ACM Press
Full text available: pdf(4.61 MB) Additional Information: full citation, references, citings, index terms

14 Parallelization, amplification, and exponential time simulation of quantum interactive proof systems

Alexei Kitaev, John Watrous
May 2000 **Proceedings of the thirty-second annual ACM symposium on Theory of computing**
Publisher: ACM Press
Full text available: pdf(1.09 MB) Additional Information: full citation, references, citings, index terms

15 A new framework for elimination-based data flow analysis using DJ graphs
Vugranam C. Sreedhar, Guang R. Gao, Yong-Fong Lee
March 1998 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 20 Issue 2
Publisher: ACM Press
Full text available: pdf(631.44 KB) Additional Information: full citation, references, citings, index terms

Keywords: DJ graphs, Tarjan's interval, exhaustive and incremental data flow analysis, irreducible flowgraphs, reducible flowgraphs

16 Post-mortem black-box correctness tests for basic parallel data structures
Phillip B. Gibbons, John L. Bruno, Steven Phillips
June 1999 **Proceedings of the eleventh annual ACM symposium on Parallel algorithms and architectures**
Publisher: ACM Press
Full text available: pdf(1.35 MB) Additional Information: full citation, references, index terms

17 Bisimulation can't be traced
Bard Bloom, Sorin Istrail, Albert R. Meyer
January 1995 **Journal of the ACM (JACM)**, Volume 42 Issue 1
Publisher: ACM Press
Full text available: pdf(2.33 MB) Additional Information: full citation, abstract, references, citings, index terms

In the concurrent language CCS, two programs are considered the same if they are bisimilar. Several years and many researchers have demonstrated that the theory of bisimulation is mathematically appealing and useful in practice. However, bisimulation makes too many distinctions between programs. We consider the problem of adding operations to CCS to make bisimulation fully abstract. We define the class of GSOS operations, generalizing the style and technical advantages of C ...

Keywords: CCS, bisimulation, process algebra, structural operational semantics

18 A linear time algorithm for placing &phgr;-nodes
Vugranam C. Sreedhar, Guang R. Gao
January 1995 **Proceedings of the 22nd ACM SIGPLAN-SIGACT symposium on Principles of programming languages**
Publisher: ACM Press
Full text available: pdf(1.35 MB) Additional Information: full citation, abstract, references, citings, index terms

Dataflow analysis framework based on Static Single Assignment (SSA) form and Sparse Evaluation Graphs (SEGs) demand fast computation of program points where data flow information must be merged, the so-called &fgr;-nodes. In this paper, we present a surprisingly simple algorithm for computing &fgr;-nodes for arbitrary flowgraphs (reducible or irreducible) that runs in linear time. We employ a novel program representation—the DJ graph—by ...

19 A brief survey of benchmark usage in the architecture community

Thomas M. Conte, Wen-mei W. Hwu
July 1991 **ACM SIGARCH Computer Architecture News**, Volume 19 Issue 4
Publisher: ACM Press
Full text available: pdf(452.61 KB) Additional Information: full citation, index terms

20 Executing formal specifications: the ASTRAL to TRIO translation approach
Carlo Ghezzi, Richard A. Kennerer
October 1991 **Proceedings of the symposium on Testing, analysis, and verification**
Publisher: ACM Press
Full text available: pdf(1.05 MB) Additional Information: full citation, references, citings, index terms

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